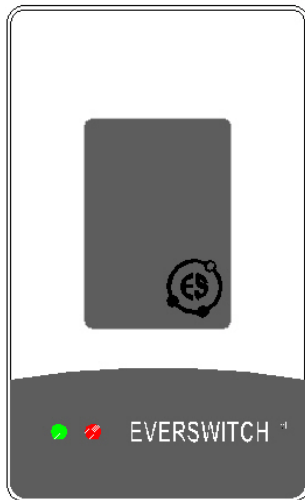


ACCESS CONTROL **ASP PROXIMITY ONLY**



PROXIMTY READER

Model: **ASP-PROXIMITY ONLY**

Output formats: Wiegand depending on Card.

Power Supply 8 to 15 VDC

Power Consumption: Max 150 mA

Transmitter/Receiver frequency: 125Khz HID

Mounting: Universal USA and EUROPE

Environment: IP68 ; 100% relative humidity

Operating Temperature: - 30 to + 65°C (-22 to 150 F)

Dimensions: 115/70/20 (4,52"/2,75"/0,78")

Antitamper Optical protection

Certifications: Under procedure

Operation Modes:

1. Present an HID encrypted Prox card. The unit will read the content and send it over the Data wires to the host.

Verification

Power up the unit. The unit will activate the buzzer. In the same time the RED or the Green LED will come ON depending on the Central Computer setup.

Present a valid HID encrypted card at 5 cm distance maximum. The unit will activate the Buzzer once.

Wiring

COLOR	FUNCTION	ELECTRICAL FUNCTION
RED	Input Voltage	8 to 15 VDC
BLACK	Ground	
GREEN	Data 0	Open collector 1Kohm pull-up to internal +5V
WHITE	Data 1	Open collector 1Kohm pull-up to internal +5V
BROWN	LED Input	No Voltage
VIOLET	Housing Ground	
ORANGE	Buffered Input	
GREY	Tamper Output	Open collector 0,100 A "Low" when light sensed

ACCESS CONTROL **ASP PROXIMITY ONLY**

26 BIT WIEGAND SPECIFICATIONS

When the LED control input is pulled low, the GREEN LED will be ON and the RED LED will be OFF. When the input goes high the RED LED is ON and the GREEN LED is OFF. The RED or GREEN LED will flash with each key press. The LED control input is pulled to the internal +5v with a 2.2K resistor.

The data is sent at 2 millisecond. per bit with a pulse duration of 70 µsec. A Buzzer beeps with each key press.

DATA FORMAT

Card data 26 WIEGAND output format.

P S S S S S S S S N N N N N N N N N N N N N N N P
BIT 1 2 9 10 25 26
BIT 1 is an even parity for the following 12 bits. The sum of bits 1-13 is even.
BITS 2-9 are the F/C the card presented from 000 to 255.
BITS 10-25 this is the card number presented .
Leading 0's are added as required. Bit 10 is most significant.
BIT 26 Odd parity over previous 12 bits. The sum of bits 14-26 is odd.

EXAMPLE: A card code of 123 entered:

1 0 0 0 0 0 1 0 0 0 0 0 0 0 0 0 1 1 1 1 0 1 1 1 (F/C 004)

The data is sent at 2 msec per bit with pulse duration of 70 µsec. A Buzzer beeps each time card is presented.

1. **Orange Wire** - When the Hold Line, Orange wire, is pulled "low", any codes entered on the keypad are stored in the buffer. When the Hold Line is released to logic "high" – the buffered code data is sent
2. **Grey Wire** - When the photodiode senses ambient light the wire is pulled "LOW"